

In the Claims:

Please amend claims 1, 7, 8, 9, 10, 11, 12, and 13 as follows:

1. (currently amended) A method for implementing a level bias function for branch prediction control for generating test simulation vectors comprising the steps of:

providing a universal set of values for a branch operand field BO;

receiving user selected options for a set of constraints for generating test simulation vectors for branch conditional instructions;

reading current resource values for predicting a branch for a branch conditional instruction; and

reducing said branch operand field BO to include only a set of valid values using said current resource values and based upon said user selected constraints; said branch operand field BO defining conditions under which a branch is taken for said branch conditional instruction.

2. (original) A method for implementing a level bias function for branch prediction control as recited in claim 1 wherein the step of receiving user selected options for a set of constraints includes the steps of receiving user selected options for constraints including a percentage branch should be taken; a percentage branch should be predicted; and an accuracy of the prediction.

3. (original) A method for implementing a level bias function for branch prediction control as recited in claim 1 wherein the step of reading current resource values for predicting a branch for a branch conditional instruction includes the steps of reading a count register (CTR) value and a plurality of branch condition register (CR)

values; said CR values including a current value of said branch operand field BO, and a current value of a branch operand field BI, said branch operand field BI indicating a CR bit to be read.

4. (original) A method for implementing a level bias function for branch prediction control as recited in claim 3 wherein the step of generating said branch operand field to include said set of valid values using said current resource values and based upon said user selected constraints includes the step of reducing said branch operand field BO based upon a current CR value of said branch operand field BI and based upon said user selected constraints.

5. (original) A method for implementing a level bias function for branch prediction control as recited in claim 3 wherein the step of generating said branch operand field to include said set of valid values using said current resource values and based upon said user selected constraints includes the steps of reducing said branch operand field BO based upon said a current count register (CTR) value and said user selected constraints.

6. (original) A method for implementing a level bias function for branch prediction control as recited in claim 2 wherein the step of generating said branch operand field to include said set of valid values using said current resource values and based upon said user selected constraints includes the steps of reducing said branch operand field BO based upon user selected options for said percentage branch should be predicted and said accuracy of the prediction constraints.

7. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control for generating test simulation vectors in a computer system, said computer ~~program-product~~ readable medium including instructions stored on a said computer recording readable medium consisting one of a floppy disk, a high capacity read only memory in the form of an optically read compact disk or CD-ROM, and a tape, wherein said instructions, when executed by the computer system cause the computer system to perform the steps of:

- providing a universal set of values for a branch operand field BO;
- receiving user selected options for a set of constraints for generating test simulation vectors for branch conditional instructions;
- reading current resource values for predicting a branch for a branch conditional instruction; said current resource values including a count register (CTR) value and a plurality of branch condition register (CR) values; and
- reducing said universal set for said branch operand field BO to include only a set of valid values using said current resource values and based upon said user selected constraints; said branch operand field defining conditions under which a branch is taken for said branch conditional instruction.

8. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control as recited in claim 7 wherein the step of receiving user selected options includes the step of receiving user selected options for said set of constraints including a percentage branch should be taken; a percentage branch should be predicted; and an accuracy of the prediction.

9. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control as recited in claim 7 wherein the step reading current resource values includes the steps of reading a current CR value of said branch operand field BO, a current CR value of a branch operand field BI and a current CTR value.

10. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control as recited in claim 9 wherein the step generating said branch operand field BO to include said set of valid values using said current resource values and based upon said user selected constraints includes the step of reducing said branch operand field BO based upon said current CR value of said branch operand field BI and based upon said user selected constraints.

11. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control as recited in claim 9 wherein the step generating said branch operand field BO to include said set of valid values using said current resource values and based upon said user selected constraints includes the step of reducing said branch operand field BO based upon said current CTR value and based upon said user selected constraints.

12. (currently amended) A computer ~~program-product~~ readable medium for implementing a level bias function for branch prediction control as recited in claim 8 wherein the step generating said branch operand field BO to include said set of valid values using said current resource values and based upon said user selected

constraints includes the step of reducing said branch operand field BO based upon based upon said user selected options for said percentage branch should be predicted and said accuracy of the prediction constraints.

13. (currently amended) Apparatus for implementing an operand level bias for branch prediction control for generating test simulation vectors comprising:

a user interface for receiving user selected options for a set of constraints for generating test simulation vectors for branch conditional instructions;

a count register (CTR) and a branch condition register (CR) for storing current resource values for predicting a branch for a branch conditional instruction; and

a level bias function for providing a universal set of values for a branch operand field BO of said branch condition register (CR) and for reducing said universal set for branch operand field BO to include only a set of valid values using said current resource values and based upon said user selected constraints; said branch operand field BO defining conditions under which a branch is taken for said branch conditional instruction.

14. (original) Apparatus for implementing an operand level bias for branch prediction control as recited in claim 13 wherein set of constraints includes a percentage branch should be taken; a percentage branch should be predicted; and an accuracy of the prediction.

15. (original) Apparatus for implementing an operand level bias for branch prediction control as recited in claim 13 wherein said branch condition register (CR) stores current resource values including a current value of said branch operand field BO

and a current value of a branch operand field BI, said branch operand field BI indicating a CR bit to be read; and said count register (CTR) stores a current count value.

16. (original) Apparatus for implementing an operand level bias for branch prediction control as recited in claim 15 wherein said level bias function generates said branch operand field BO using said current value of said branch operand field BI and based upon said user selected constraints to reduce said current value of said branch operand field BO to include said set of valid values.

17. (original) Apparatus for implementing an operand level bias for branch prediction control as recited in claim 15 wherein said level bias function generates said branch operand field BO using said current count value of said count register (CTR) and based upon said user selected constraints to reduce said current value of said branch operand field BO to include said set of valid values.

18. (original) Apparatus for implementing an operand level bias for branch prediction control as recited in claim 14 wherein said level bias function generates said branch operand field BO using said percentage branch should be predicted and said accuracy of the prediction constraints to reduce said current value of said branch operand field BO to include said set of valid values.